

FIG. 1

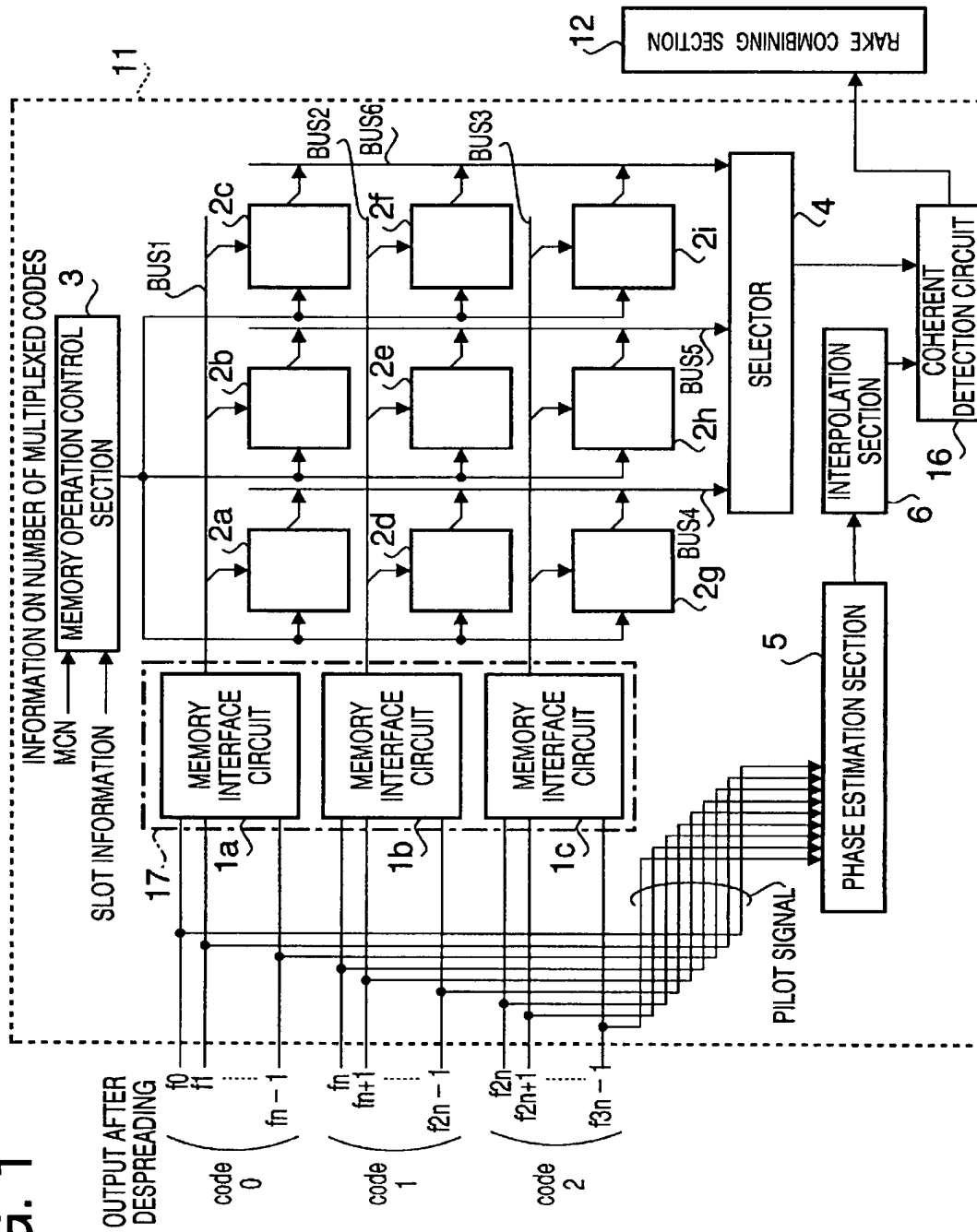


FIG. 1

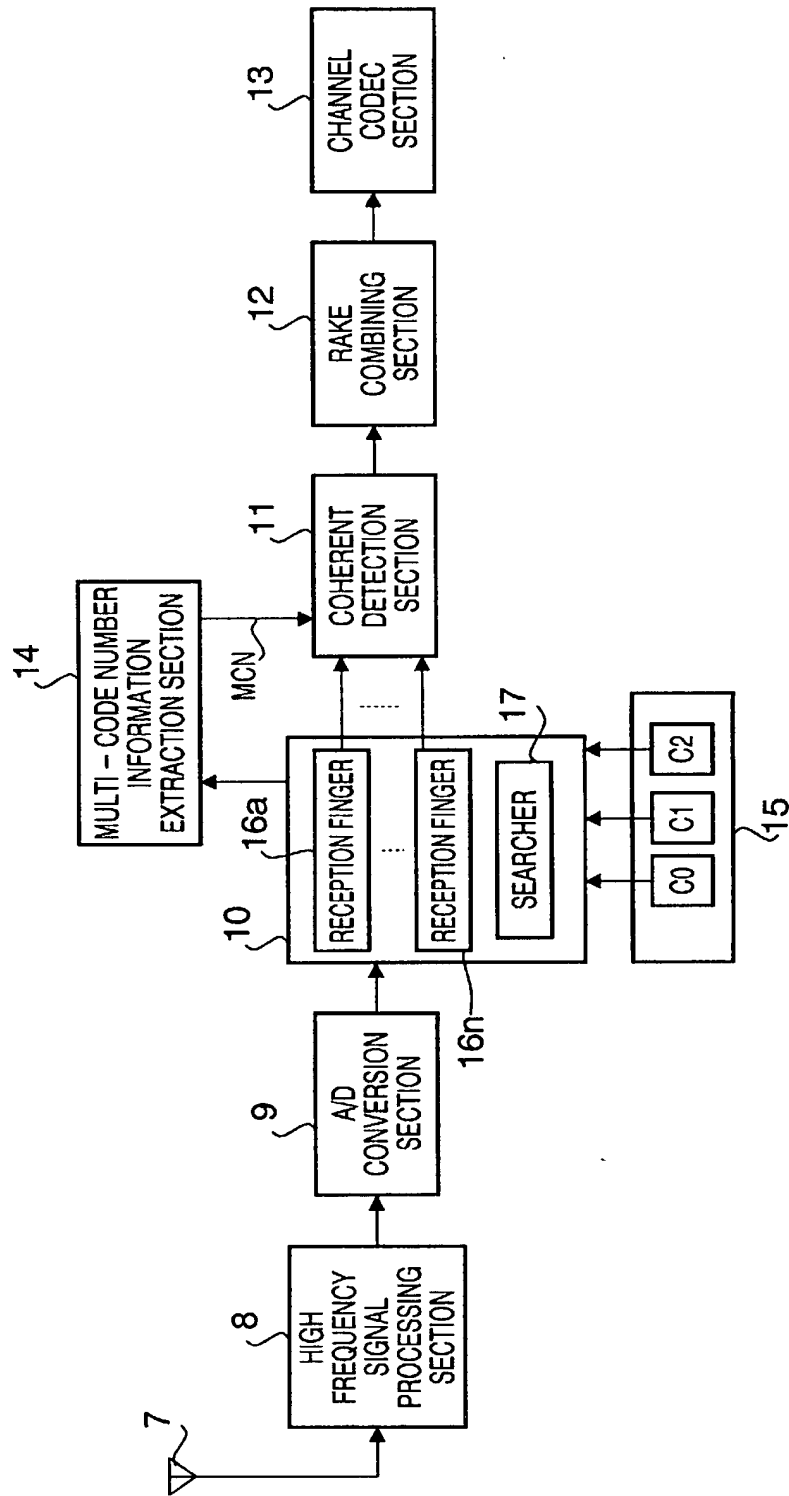
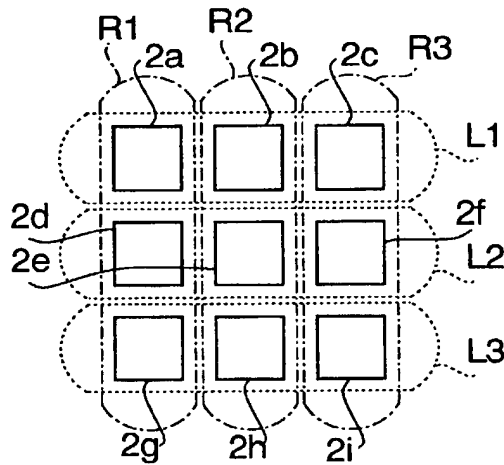


FIG. 2

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R1 TO R3 : SLOT - SPLIT MEMORY
 BLOCK ROWS
 L1 TO L3 : CODE - SPLIT MEMORY
 BLACK LINES

FIG. 3A

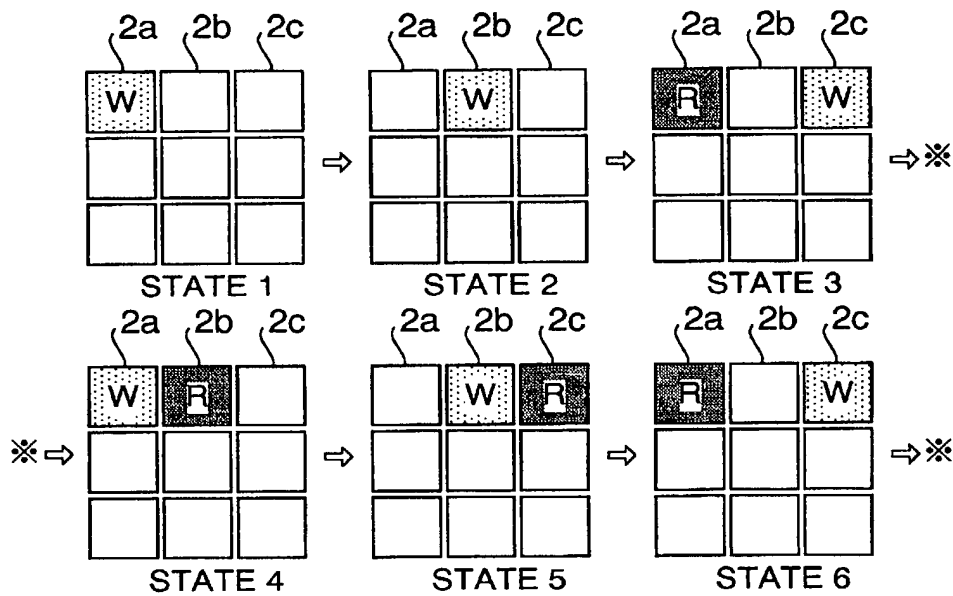


FIG. 3B

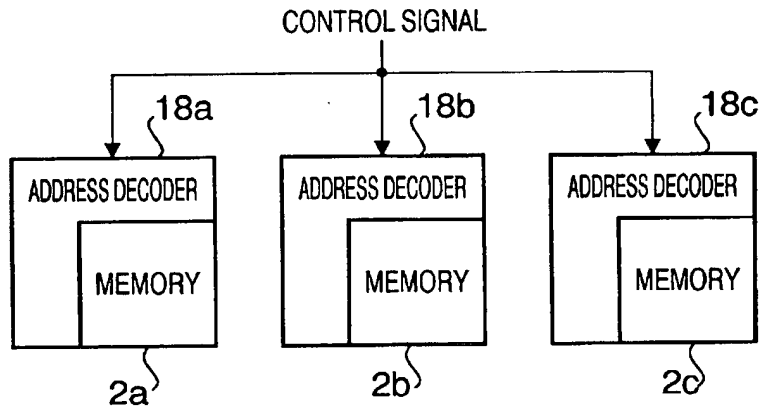


FIG. 4A

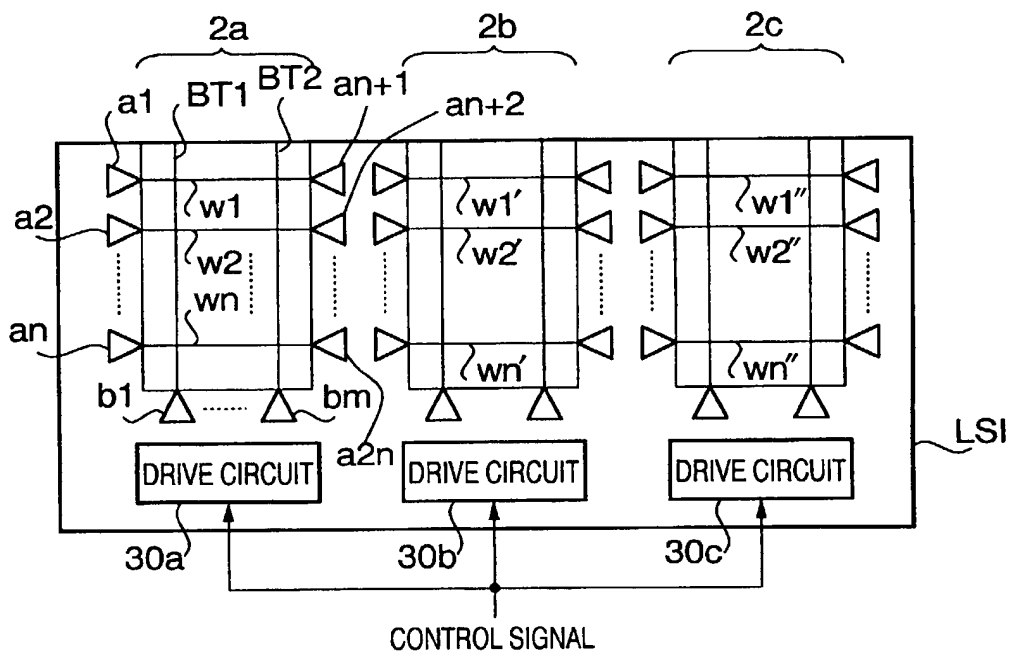


FIG. 4B

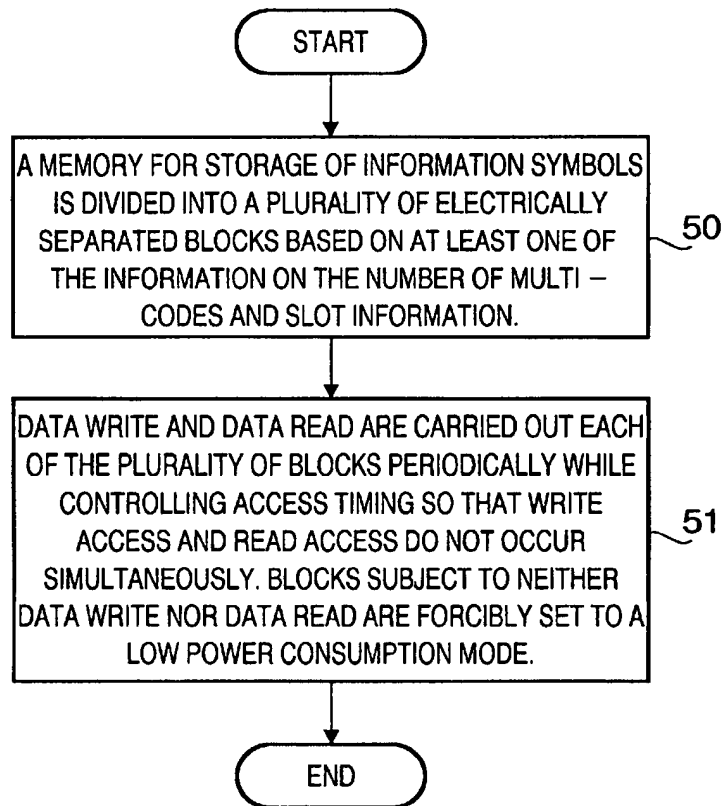


FIG. 5

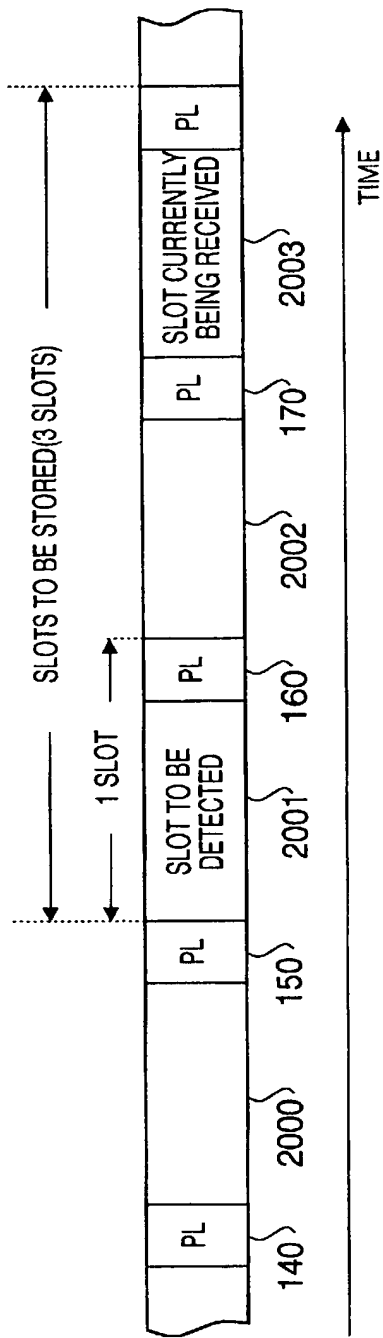


FIG. 6